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for AI Electronics

英語論文の書き方に関する 講習会のお知らせ

Easy and lazy technical writing for engineers and scientists : A step-by-step guide to establish a good logical structure

This talk presents an easy and lazy way to prepare technical papers in a step-by-step manner targeting at novices such as entry-level engineers and young scientists including students. As a solution to a good logic, which is uneasy for unexperienced authors to build, "Slides First" is proposed and explained in details with examples. Slides help the authors build a solid logic for the paper, which directly benefits them by making reviewers understand the point of the paper easily and clearly, leading to a higher review score. There is a specific order of preparation to make the process most efficient. The highlight is a three-point analysis for which templates are provided to clarify a value, a trick, and a user benefit of the paper. These three points to support a good logic naturally lead to a good paper title that can also be used in the abstract. This lecture is unique in the point that it tells how drafting should proceed, not simply what a draft should include, which is not available in technical writing books and talks.

講師

合同会社 ダマス.カス代表

Dr. Akihiko Sugiyama

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7/01 MON

16:00 - 17:30

電子情報システム・応物系
復興記念教育研究未来館
2階 PBL室(213室)
【オンライン併用】

略歴: Akihiko Sugiyama (a.k.a. Ken Sugiyama), affiliated with Yahoo! JAPAN Research, has been engaged in a wide variety of research projects in signal processing such as audio coding and interference/noise control. Prior to Yahoo Japan, he had a long career at NEC Corporation as a research engineer. His team at NEC developed the world's first all solid-state portable audio player, the Silicon Audio, in 1994, a precursor of iPod. He served as the Chair of Audio and Acoustic Signal Processing Technical Committee, IEEE Signal Processing Society (SPS) [2011-2012], as associate editors for several journals such as IEEE Trans. Signal Processing [1994-1996], as the Secretary and a Member at Large to the Conference Board of SPS [2010-2011], as a member of the Awards Board of SPS [2015-2017], as the Chair of Japan Chapter of SPS [2010-2011], and a member of IEEE Fellow Committee [2018-2020]. He was a Technical Program Chair for ICASSP2012. He has contributed to 17 chapters of books and is the inventor of 217 registered patents with more pending applications in the field of signal processing in Japan and overseas. He received 19 awards such as the 2002 IEICE Best Paper Award, the 2006 and 2018 IEICE Achievement Award, and the 2013 Ichimura Industry Award. He has delivered 153 invited talks in 85 cities of 29 countries. He is Fellow of IEEE and IEICE, a Distinguished Industry Speaker for IEEE SPS [2020-2021], a Renowned Distinguished Speaker for IEEE CE (Consumer Electronics Society, Class of 2020) and was a Distinguished Lecturer for IEEE SPS [2014-2015] and for IEEE CE [2017-2018].

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アクセス



参加申込

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